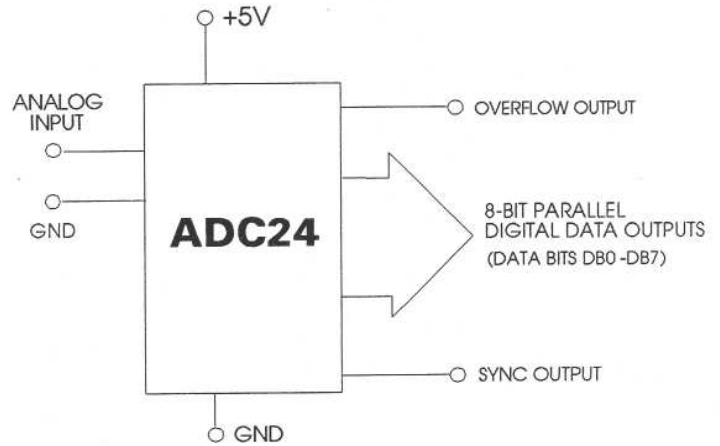
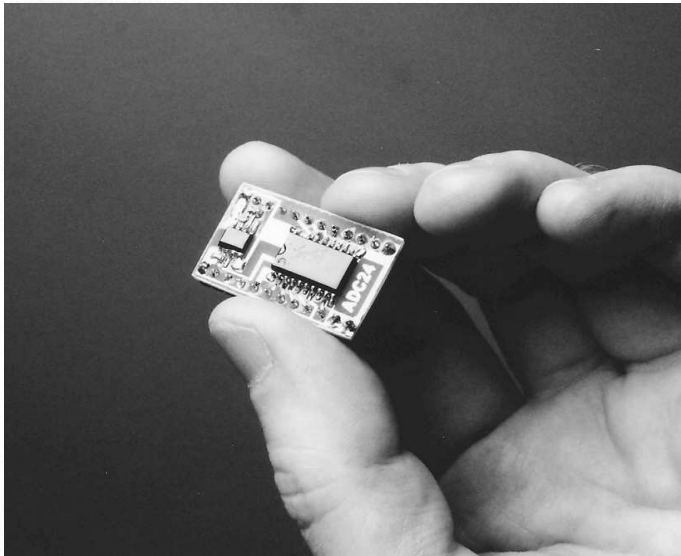


ADC24

8-BIT HYBRID CMOS HYBRID ANALOG TO DIGITAL CONVERTER



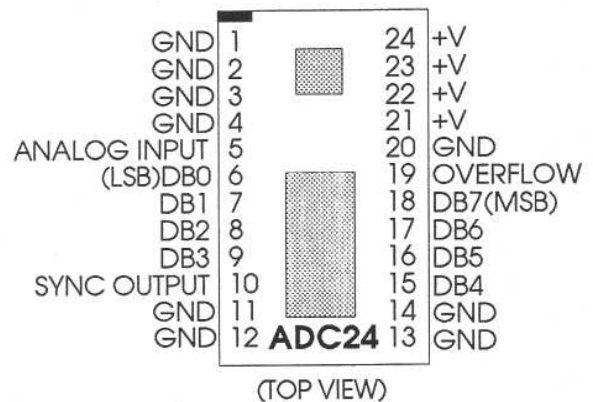
FUNCTIONAL DIAGRAM

FEATURES

- EASY TO USE
- SINGLE 5 VOLT SUPPLY OPERATION
- MINIATURE SIZE AND WEIGHT
- 8-BIT RESOLUTION
- LOW POWER CONSUMPTION (<100mW Typ.)
- 100% PERFORMANCE TESTED
- DIGITIZES SIGNALS FROM D.C. TO 60 KHz

GENERAL DESCRIPTION-

THE ADC24 IS A HYBRID CMOS INTEGRATED CIRCUIT THAT REDUCES AN ENTIRE A/D CONVERSION SYSTEM TO A 24-PIN DIP I.C. A SINGLE 5 VOLT SUPPLY OPERATES THE CONVERTER WITHOUT CLOCK OR CONTROL INPUTS, MAKING IT THE EASIEST TO USE A/D EVER. THE INNOVATION CAUSES A DRAMATIC REDUCTION IN OVERALL SYSTEM SIZE, WEIGHT, POWER CONSUMPTION, AND COST. INTERFACING WITH MICROPROCESSORS IS EASILY PERFORMED AND REQUIRES LESS PROTOCOL USING THE SYNC OUTPUT. AN ADC24 ANALOG TO DIGITAL CONVERTER WILL DELIVER HIGH TRUE VALID DIGITAL DATA OUTPUTS WITH AN ANALOG INPUT RANGE OF DC TO 60 KILOHERTZ. LINEARITY IS 1 LSB OR BETTER.



PIN LAYOUT DIAGRAM

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www.elkindustries.com

ADC 24

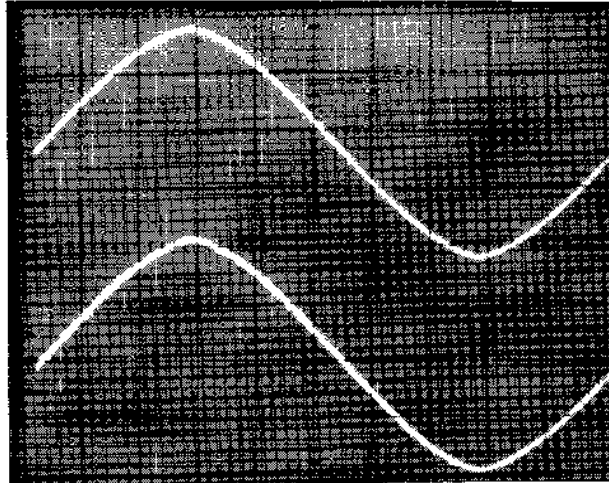
R - K MANUFACTURING
CMOS INTEGRATED CIRCUIT

R-K MANUFACTURING IS A DIVISION OF ELK INDUSTRIES

TEST DATA

NOTE: Digitized analog signals, reconstructed via D/A conversion.

1 KHz

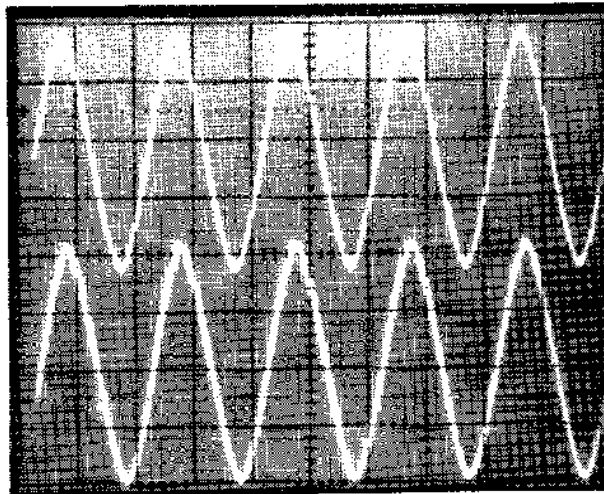


INPUT SIGNAL
TRACE 1
1 v/Div.

OUTPUT SIGNAL
TRACE 2
2 v/Div.

Time Base 0.1mS/Div.

20 KHz



INPUT SIGNAL
TRACE 1
1 v/Div.

OUTPUT SIGNAL
TRACE 2
2 v/Div.

Time Base 20μS/Div.

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